Drafts
  BRS:
  ISNR:
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  BRS: 708/490.ccls. and 6
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Pending
Active
  L1: (10537) array$1 near3 (mac$1 or processor$1)
  L2: (12022) local adj memory
  L3: (16427) (share$1 or global) adj memory
  L4: (32) 1 same 2 same 3
  L5: (260) 1 and 2 and 3
  L6: (228) 5 not 4
  L7: (121) 6 and (mac$1 or multiply$3)
  L8: (107) 6 not 7
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Default operator OR   highlight all hit terms initially

6 not 7

BRS term ISR term Image Text HTML

	U	[1]	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	
1			US 20040049672	20040311	31	System and method for hardware-software multitasking on a reconfigurable computing platform	713/100			Nollet
2			US 20040015735	20040122	44	Fault tolerant cell array architecture	714/10	714/25; 714/29;		Norm
3			US 20040015633	20040122	17	Signal processing resource for selective series processing of data in transit on communications paths in multi-processor arrangements	710/305			Smith
4			US 20040012563	20040122	24	Systems and methods of multi-pass data processing	345/157			Papak et al.
5			US 20030210686	20031113	70	ROUTER AND METHODS USING NETWORK ADDRESSES FOR VIRTUALIZATION	370/389	370/400		Terrel
			US 20021023		20	Providing a register file memory with local addressing in a SIMD parallel	711/1	365/230.06		Kim

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